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1.	Authors:	Vibha Mishra, Vinod Kapse	
	Paper Title:	Hardware Implementation of 64 Bit Floating Point Arithmetic Using VHDL	
	<p>Abstract: Many of the scientific applications rely on floating point (FP) computation, often requiring the use of the 64 bit Floating Point format specified by the IEEE standard 754. The use of double precision (D.P.) data type improves the accuracy and dynamic range of the computation, but simultaneously it increases the complexity and performance of the arithmetical computation of the module. The design of high performance 64-Bit floating point units (FPUs) is thus of interest in this Document.</p> <p>Keywords: IEEE, (D.P.) (FP).</p> <p>References:</p> <ol style="list-style-type: none"> 1. P. Belanovic and M. Leaser, —A Library of Parameterized Floating-Point Modules and Their Usel, in 12th International Conference on Field-Programmable Logic and Applications (FPL- 02). London, UK: Springer-Verlag, (2002) September, pp. 657–666. 2. K. Hemmert and K. Underwood, —Open Source High Performance Floating-Point Modulesl, in 14th Annual IEEE Symposium on Field-Programmable Custom Computing Machines (FCCM-06), (2006) April, pp. 349–350. 3. A. Malik and S. -B. Ko, —A Study on the Floating-Point Adder in FPGAsl, in Canadian Conference on Electrical and Computer Engineering (CCECE-06), (2006) May, pp. 86–89. 4. D. Sangwan and M. K. Yadav, —Design and Implementation of Adder/Subtractor and Multiplication Units for Floating-Point Arithmeticl, in International Journal of Electronics Engineering, (2010), pp. 197-203. 5. M. K. Jaiswal and R. C. C. Cheung, —High Performance FPGA Implementation of Double Precision Floating Point Adder/Subtractorl, in International Journal of Hybrid Information Technology, vol. 4, no. 4, (2011) October. 6. B. Lee and N. Burgess, —Parameterisable Floating-point Operations on FPGA, Conference Record of the Thirty-Sixth Asilomar Conference on Signals, Systems, and Computers, (2002). 7. M. Al-Ashrafy, A. Salem, W. Anis, —An Efficient Implementation of Floating Point Multiplierl, Saudi International Electronics, Communications and Photonics Conference (SIECPC), (2011) April 24-26, pp. 1-5. 		1-4
2.	Authors:	R. Dhayabarani, R. S. D. Wahida Banu	
	Paper Title:	Performance Analysis of Multiplier using Full Adder	
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3.	Authors:	Neelam T. Rakate, U. A. Patil	
	Paper Title:	Iris Biometric for Person Identification Using Dual- Tree Complex Wavelet Transform	
	<p>Abstract: Technologies that exploit biometrics have the potential for application to the identification and verification of individuals for controlling access to secured areas or materials. A wide variety of biometrics has been marshaled in support of this challenge. Resulting systems include those based on automated recognition of retinal vasculature, fingerprints, hand shape, handwritten signature, and voice. Unfortunately, from the human factors point of view, these systems are highly invasive. One possible alternative to these methods that has the potential to be less invasive is automated iris recognition. Interestingly, the spatial patterns that are apparent in the human iris are highly distinctive to an individual. The iris has unique features and is complex enough to be used as a biometric signature. Therefore, in order to use the iris pattern for identification, it is important to define a representation that is well adapted for extracting the iris information content from images of the human eye. Here we represent a new algorithm for extracting unique features from images of the iris of the human eye and representing these features using two-dimensional dual-tree complex wavelet transform (DTCWT). This representation is then utilized to recognize individuals from images of the irises of their eyes. The proposed technique is translation & shift invariant. For the dual filter tree, we have selected two linear phase biorthogonal filter sets of same lengths (based on Selesnick’s approach) which are used to filter each signal for quantization to 375 byte iris feature codes. Then the Hamming distance is used to match two iris codes. The experimental results on UPOL database shows good reliability and performance, so it is promising to be used in a personal identification system.</p> <p>Keywords: Biometrics, Complex Wavelet Transform, Feature extraction, Hamming distance.</p> <p>References:</p> <ol style="list-style-type: none"> 1. A. S. Narote, S. P. Narote, L. M. Waghmare and M. B. Kokare, “Robust iris feature extraction using dual tree complex wavelet transform,” 2007, IEEE International Conference on Signal Processing and Communications (ICSPC 2007), 24-27 November 2007, Dubai, United Arab Emirates. 2. Waheeda Almayyan , Hala S. Own, Hussein Zedan, “Iris Features Extraction using Dual-Tree Complex Wavelet Transform,” International Conference of Soft Computing and Pattern Recognition 2010. 3. Ivan W. Selesnick, Richard G. Baraniuk, and Nick G. Kingsbury, “The Dual-Tree Complex Wavelet Transform,” IEEE Signal processing magazine, pp.: 123-152, November 2005. 4. Rajesh M. Bodade, Dr. Sanjay N. Talbar, “Iris Recognition using Combination of Dual Tree Rotated Complex Wavelet and Dual Tree Complex Wavelet,” IEEE ICC 2009 proceedings. 5. J. Daugman, “High Confidence Visual Recognition of Persons by a Test of Statistical Independence,” IEEE Trans. on Pattern Analysis and Machine Intelligence, Vol. 15, No.11, pp.1148-1161, 1993. 6. G Kaiser, “A Friendly Guide to Wavelets”, Birkhauser, Boston, 1994. 		9-18

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	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 20%;">Authors:</td> <td>C. Jena, Amruta Das, C. K. Panigrahi, M. Basu</td> </tr> <tr> <td>Paper Title:</td> <td>Modelling and Simulation of Photovoltaic Module with Buck-Boost Converter</td> </tr> </table>	Authors:	C. Jena, Amruta Das, C. K. Panigrahi, M. Basu	Paper Title:	Modelling and Simulation of Photovoltaic Module with Buck-Boost Converter	
Authors:	C. Jena, Amruta Das, C. K. Panigrahi, M. Basu					
Paper Title:	Modelling and Simulation of Photovoltaic Module with Buck-Boost Converter					
4.	<p>Abstract: This paper presents a unique step-by-step procedure for the simulation of photovoltaic modules with Matlab/ Simulink. The objective is to design & simulate a controller for the unlimited solar power drawn from the sun & produce a higher voltage o/p through the d.c. to d.c. (Buck-boost) converter .One-diode equivalent circuit is employed in order to investigate i-v and p-v characteristics of a typical 36W solar module. The proposed module is designed with different icons, dialogue box like simulink block libraries. This PV module is interfaced to the buck boost converter and the performance has been studied by the matlab simulink.</p> <p>Keywords: Photovoltaic (PV), Buck-Boost Converter, simulation of PV model, simulation results.</p> <p>References:</p> <ol style="list-style-type: none"> 1. J.A. Ramos-HernanZ J.J. Campayo 1 J. Larranaga 2 E. Zulueta 3 O. Barambones 3 J. Motrico 1 U. Fernandez Gamiz 4 I. Zamora 1-TWO PHOTOVOLTAIC CELL SIMULATION MODELS IN MATLAB/SIMULINK.International Journal on "Technical and Physical Problems of Engineering". 2. Mathematical Modelingof Photovoltaic Module with Simulink.N. Pandiarajan and RanganathMuthu Department of Electrical & Electronics Engineering.International Conference on Electrical Energy Systems (ICEES 2011), 3-5 Jan 2011. 3. Maximum Power Point Tracking For Photovoltaic System by Perturb and Observe Method Using Buck Boost Converter.M.S.Sivagamasundari1, Dr.P.Melba Mary2,V.K.Velvizhi3. International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering Vol. 2, Issue 6, June 2013. 4. M. G. Villalva, J. R. Gazoli, E. Ruppert F, "Comprehensive approach to modeling and simulation of photovoltaic arrays", IEEE Transactions on Power Electronics, 2009 vol. 25, no. 5, pp. 1198--1208, ISSN 0885-8993. 5. P. S. Revankar, W. Z. Gandhare and A. G. Thosar Government College of Engineering, Aurangabad, "Maximum Power Point Tracking for PV Systems Using MATLAB/SIMULINK", 2010 Second International Conference on Machine Learning and Computing. 6. Hassan Abouobaida,MohamedCherkaoui, Department of Electrical Engineering, EcoleMohamediad'ingenieur, Mohamed V University, Rabat, Morocco, "Comparative Study of Maximum Power Point Trackers for Fast Changing Environmental Conditions". 978-1•4673-1520-3/12/ 2012 IEEE. 7. A NEW APPROACH OF MODELLING, SIMULATION OF MPPT FOR PHOTOVOLTAICSYSTEM IN SIMULINK MODEL.M. Abdulkadir, A. S. Samosir, A. H. M. Yatim and S. T. Yusuf Department of Energy Conversion, Faculty of Electrical Engineering, Universiti y of Technology. 8. Technical and Economic Modeling of the 2.5kW Grid-Tie Residential PhotovoltaicC. Chukwuka*, K.A. Folly*Department of Electrical Engineering, University of Cape Town .INTERNATIONAL JOURNAL of RENEWABLE ENERGY RESEARCH K.A. Folly et al., Vol.3, No.2, 2013. 9. G. Walker, "Evaluating MPPT Converter Topologies Using a Matlab PV Model", Journal of Electrical and Electronics Engineering, Australia, Vol. 21, No. 1, pp. 49-56, 2001. 10. M.G. Villalva, J.R. Gazoli, E. Ruppert "Modeling and Circuit Based Simulation of Photovoltaic Arrays", Brazilian Journal of Power Electronics, Vol. 14, No. 1, pp. 35-45, 2009. 11. J.A. Gow, C.D. Manning "Development of a Photovoltaic Array Model for Use in Power Electronics Simulation Studies", IEE Proceedings on Electric Power Applications, Vol. 146, No. 2, pp. 193-200, March 1999. 12. DEVELOPMENT OF A DC-DC BUCK BOOST CONVERTER USING FUZZY LOGIC CONTROL. FATHI SHABAN JABER.Faculty of Electrical and Electronic Engineering UniversitiTun Hussein Onn Malaysia M.sc Thesis. 13. Muhammad H. Rashid, "Power Electronics Circuits, Devices and Applications", Third Edition. 14. I.H Atlas, A.M Sharaf, "A photovoltaic Array Simulation Model for Matlab- Simulink GUI Environment", Proce. of IEEE International Conference on Clean Electrical Power, ICCEP 2007, Capri, Italy. 	19-22				